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FAX No 571 273-8300
To: Examiner Helene Tayong, phone (571) 270-1675
From: Mirut Dalal, Attorney of Record, Reg. # 44,052
Re: Application Serial 10/729,443

Comments:

On the first page of the drawings, references 510, 517,
and ~~525~~-515 are supposed to be in red.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Attorney Docket No. 15816US02)

In the Application of:

Gordon, et al.

Serial No. 10/729,443

Filed: 12/05/2003

For: Managing Bursts of Data

Examiner: Tayong, Helene E.

Group Art Unit: 2611

Confirmation No. 1072

Electronically Filed

SUPPLEMENTAL AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir / Madam:

This paper responds includes amendments to the specification and drawings. Attached please find: (1) Specification Amendments; (2) Drawing Amendments; (3) Remarks; and (4) Amended Drawings.

DRAFT – DO NOT ENTER

IN THE SPECIFICATION

Please Replace Paragraph 0023.1 with following paragraph:

[0023.1] Referring now to **FIGURE 5**, there is illustrated a block diagram describing an exemplary circuit 500 for decoding video data in accordance with an embodiment of the present invention. A video decoder 505 includes a function commonly referred to as "motion compensation." This function is used to allow the video decoder 505 to process numerous video compression standards, including but not necessarily limited to: MPEG-1, MPEG-2, MPEG-4, H.263, H.264, and H.261. More specifically, motion compensation includes a process of copying a two-dimensional block 510 of image data from a previously decoded reference frame 515 to the frame 520 currently being decoded. The location of the reference block 517 relative to the current position in the current frame is specified by "motion vectors" included within the input code stream 525. Motion compensation allows for a compact specification of the data whenever the video stream is well modeled by translational motion. The reference frame 515 that is used for motion compensation is stored in a relatively large memory, such as DRAM 530. The DRAM 530 can be configured to store the reference frame 515 in the manner described in FIGURES 3 and 4. In one embodiment, segments of data with better alignment with respect to the burst boundaries can be determined by combinatorial logic in the video decoder 505.

DRAFT – DO NOT ENTER

IN THE DRAWINGS

Please amend FIGURE 5 as indicated in the attached figures.

Assignee respectfully submits that no new subject matter is added.

DRAFT - DO NOT ENTER

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REMARKS

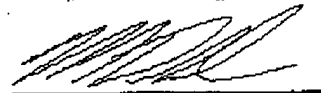
Assignee respectfully requests entry of the foregoing amendments to the specification and submits that no new subject matter is added.

CONCLUSION

For at least the foregoing reasons, Assignee respectfully submits that the application is in a condition for allowance and a notice of allowance is respectfully requested. The Commissioner is hereby authorized to charge any fees in connection with any of the actions requested herein to deposit account number 13-0017.

Dated: March 25, 2009

Respectfully submitted,



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